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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Chambers

Art Unit:

2829

Serial No.:

10/649,425

Examiner:

Sarkar, A.

TI-35714

Filing Date:

08/27/2003

Docket No.:

5169

Customer No.: 23494

Conf. No.:

Title: STRUCTURE AND METHOD TO FABRICATE SELF-ALIGNED TRANSISTORS

WITH DUAL WORK FUNCTION METAL GATE ELECTRODES

## CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that the following papers are being transmitted by facsimile to the U.S. Patent and Trademark Office at 571-273-8300 on the date shown below:

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September 16, 2005

Date

## **FACSIMILE COVER SHEET**

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| TITLE OF INVENTION:   |                    | Filing Date: 08/27/2003   |             |      |
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